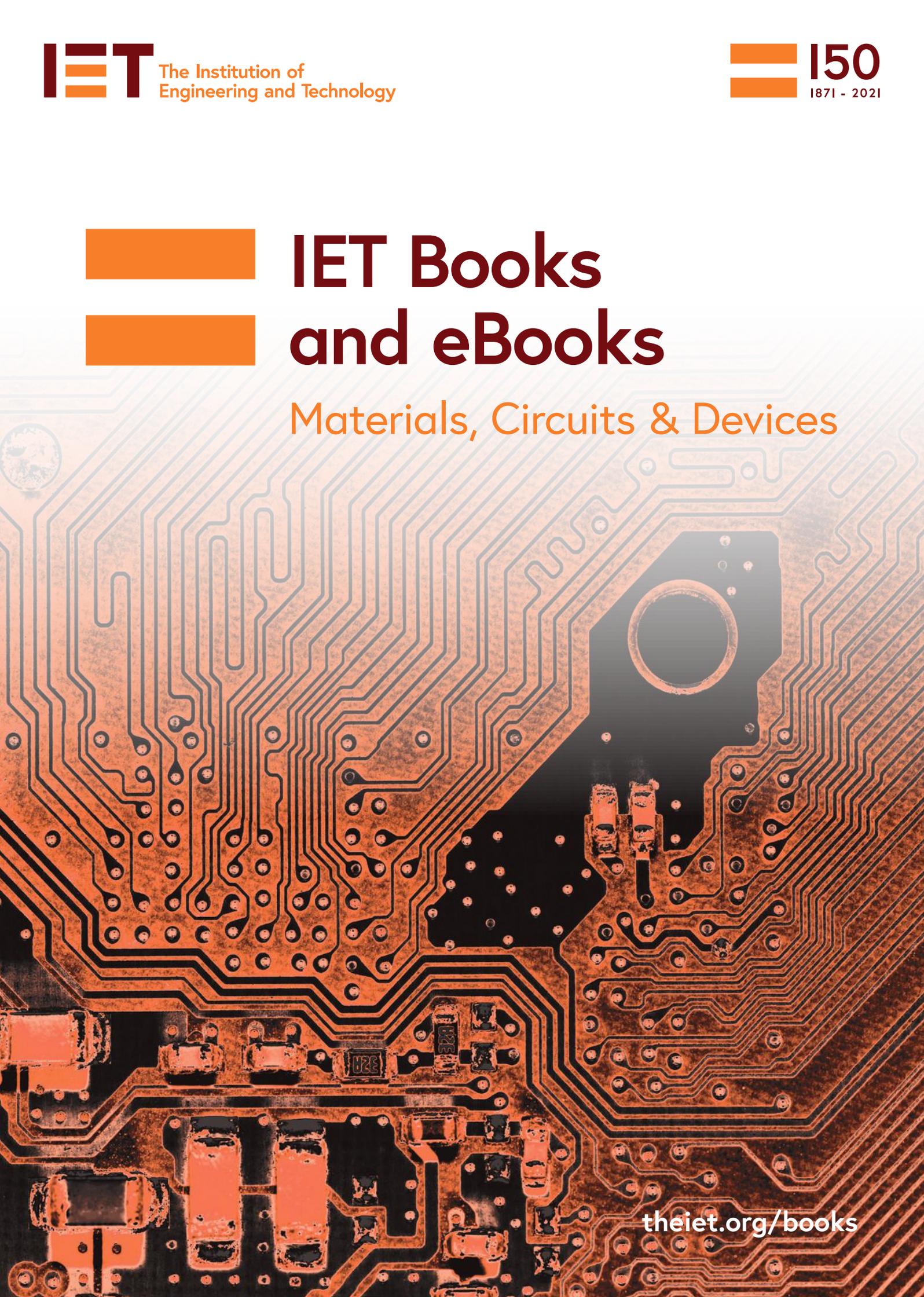




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HIGHLIGHTS

Advanced Materials and Technologies for Next Generation Integrated Circuits

Editors: Saraju P. Mohanty, University of North Texas (UNT), USA; Ashok Srivastava, Louisiana State University, USA

Novel devices with the potential to provide alternatives to existing nanometer CMOS technology are the subject of research in the fields of electronic materials and electronic design automation. This book explores the materials and design requirements of emerging integrated circuit technologies, and outlines their prospective applications.

2020 / 320pp / £115 / \$150

Print PBCS0490 / 978-1-78561-664-8

eBook PBCS049E / 978-1-78561-665-5



Analysis and Design of CMOS Clocking Circuits For Low Phase Noise

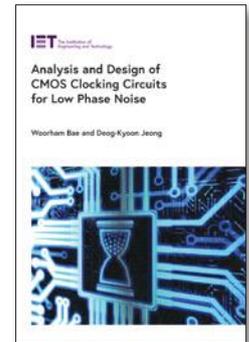
Authors: Woorham Bae, Ayar Labs, USA; Deog-Kyoon Jeong, Seoul National University, South Korea

As electronics continue to become faster, smaller and more efficient, development and research around clocking signals and circuits has accelerated to keep pace. This book bridges the gap between the classical theory of clocking circuits and recent technological advances.

2020 / 300pp / £115 / \$150

Print PBCS0590 / 978-1-78561-801-7

eBook PBCS059E / 978-1-78561-802-4



Cross-Layer Reliability of Computing Systems

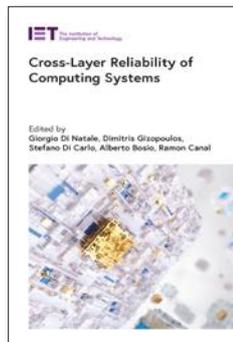
Editors: Giorgio Di Natale, National Research Center TIMA Laboratory, France; Alberto Bosio, Ecole Centrale de Lyon - INL, France; Ramon Canal, Universitat Politècnica de Catalunya-Barcelona Tech (UPC), Spain; Stefano Di Carlo, Politecnico di Torino, Italy; Dimitris Gizopoulos, National and Kapodistrian University of Athens, Greece

This book presents state-of-the-art solutions for increasing the resilience of computing systems, both at single levels of abstraction and multi-layers. It is a valuable resource for researchers, postgraduate students and professional computer architects focusing on the dependability of computing systems.

2020 / 300pp / £115 / \$150

Print PBCS0570 / 978-1-78561-797-3

eBook PBCS057E / 978-1-78561-798-0



Frontiers in Hardware Security and Trust: Theory, design and practice

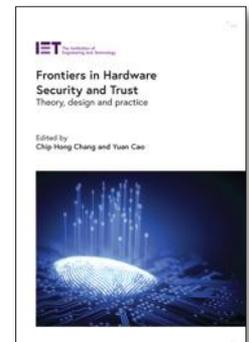
Editors: Chip Hong Chang, Nanyang Technological University, Singapore; Yuan Cao, Hohai University, China

This book provides a comprehensive review of emerging security threats and privacy protection issues, and the versatile state-of-the-art hardware-based security countermeasures proposed by the hardware security community. It serves as an advanced reference for researchers on current hardware security problems, challenges and solutions.

2020 / 440pp / £125 / \$160

Print PBCS0660 / 978-1-78561-927-4

eBook PBCS066E / 978-1-78561-928-1



HIGHLIGHTS

Frontiers in Securing IP Cores: Forensic detective control and obfuscation techniques

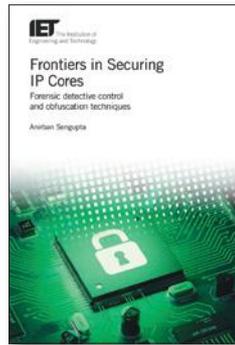
Author: Anirban Sengupta, Indian Institute of Technology Indore, India

This book presents advanced forensic detective control and obfuscation techniques for securing hardware IP cores by exploring beyond conventional technologies. The book begins by introducing forensic detective control and obfuscation mechanisms for hardware and IP core security. Further chapters cover hardware steganography, digital signature driven hardware authentication, fault-secured IP cores using digital signature-based watermarks, multi-level watermarking, cryptosystem-based multi-variable fingerprinting, multi-phase and hologram-based obfuscation, and security of functionally obfuscated DSP cores.

2019 / 344pp / £115 / \$150

Print PBCS0670 / 978-1-83953-031-9

eBook PBCS067E / 978-1-83953-032-6



Gyrators, Simulated Inductors and Related Immittances: Realizations and applications

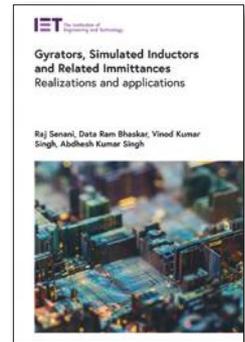
Authors: Raj Senani, Netaji Subhas Institute of Technology (NSIT), India; Data Ram Bhaskar, Jamia Millia Islamia, New Delhi, India; Vinod Kumar Singh, Institution of Engineering and Technology, Lucknow, India; Abdhesh Kumar Singh, Delhi Technical Campus, India

This book provides coverage of the major gyrator circuits, simulated inductors and related synthetic impedances. It offers a review of research in this field to date, and includes a wide range and number of circuit examples, along with their relevant design equations, limitations, performance features, advantages and shortcomings.

2020 / 576pp / £140 / \$185

Print PBCS0480 / 978-1-78561-670-9

eBook PBCS048E / 978-1-78561-671-6



Hardware Architectures for Deep Learning

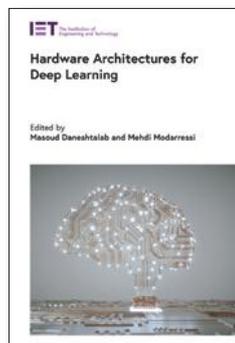
Editors: Masoud Daneshtalab, Mälardalen University (MDH), Sweden; Mehdi Modarressi, University of Tehran, Iran

This book discusses innovative ideas in the design, modelling, implementation, and optimization of hardware platforms for neural networks. The book provides an overview of this emerging field, from principles to applications, for researchers, postgraduate students and engineers who work on learning-based services and hardware platforms.

2020 / 328pp / £115 / \$150

Print PBCS0550 / 978-1-78561-768-3

eBook PBCS055E / 978-1-78561-769-0



Integrated Optics

Volume 1: Modeling, material platforms and fabrication

Volume 2: Characterization, devices, and applications

Editors: Giancarlo Righini, Enrico Fermi Center & IFAC CNR, Italy; Maurizio Ferrari, Institute for Photonics and Nanotechnologies, CNR, Italy

Edited by two recognised experts, this work provides a comprehensive overview of integrated optics, from modelling to fabrication, materials to integration platforms, and characterization techniques to applications. The technology is explored in detail, and set in a broad context that addresses a range of current and potential future research and development trends. Volume 1 addresses modeling, material platforms and fabrication of integrated optics while Volume 2 explores characterization, devices and applications.

Vol 1: 2020 / 328pp / £115 / \$150

Print PBCS077A / 978-1-83953-341-9

eBook PBCS077F / 978-1-83953-342-6

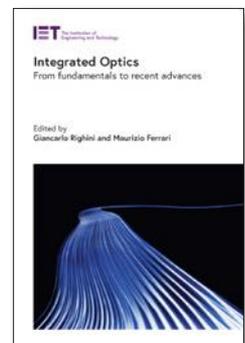
Vol 2: 2020 / 301pp / £115 / \$150

Print PBCS077B / 978-1-83953-343-3

eBook PBCS077G / 978-1-83953-344-0

Set: 2020 / £180 / \$240

Print PBCS077X / 978-1-83953-345-7



HIGHLIGHTS

MEMS Resonator Filters

Editor: Rajendra M Patrikar, VNIT Nagpur, India

This book explores the challenges and opportunities of developing circuits with MEMS resonator filters. The replacement of classical electrical components with electromechanical components is explored, and the specific properties of MEMS resonators required in various frequency ranges are discussed.

2020 / 432pp / £125 / \$160

Print PBCS0650 / 978-1-78561-896-3

eBook PBCS065E / 978-1-78561-897-0



Modelling Methodologies in Analogue Integrated Circuit Design

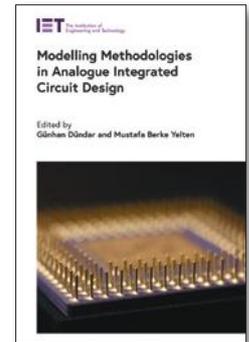
Editors: Günhan Dündar, Bogazici University, Turkey; Mustafa Berke Yelten, Istanbul Technical University, Turkey

This book provides a holistic view of modelling for analogue, high frequency, mixed signal, and heterogeneous systems for designers working towards improving efficiency, reducing design times, and addressing the challenges of representing aging, variability, and other technical challenges at the nanometre scale.

2020 / 320pp / £115 / \$150

Print PBCS0510 / 978-1-78561-695-2

eBook PBCS051E / 978-1-78561-696-9



Phase-Locked Frequency Generation and Clocking: Architectures and circuits for wireless and wireline systems

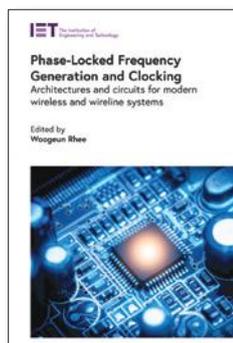
Editor: Woogeun Rhee, Tsinghua University, China

This book covers essential topics and issues in current Phase-Locked Loop design, from fundamentals to practical design aspects. Both wireless and wireline systems are considered in the design of low noise frequency generation and clocking systems.

2020 / 736pp / £140 / \$180

Print PBCS0640 / 978-1-78561-885-7

eBook PBCS064E / 978-1-78561-886-4



Secured Hardware Accelerators for DSP and Image Processing Applications

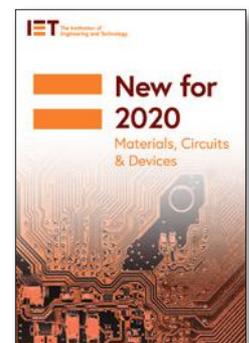
Anirban Sengupta, Indian Institute of Technology Indore, India

Written by an acknowledged expert in the field, this book focuses on approaches for designing secure hardware accelerators for digital signal processing and image processing. State-of-the art security and optimization algorithms are presented, and their roles in the design of secured hardware accelerators explored.

2020 / 300pp / £115 / \$150

Print PBCS0760 / 978-1-83953-306-8

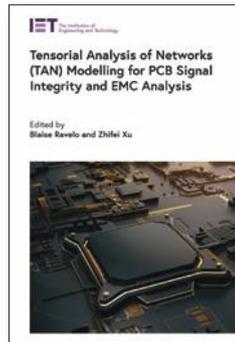
eBook PBCS076E / 978-1-83953-307-5



HIGHLIGHTS

Tensorial Analysis of Networks (TAN) Modelling for PCB Signal Integrity and EMC Analysis

Editors: Blaise Ravelo, NUIST University, China; Zhifei Xu, Missouri S&T EMC Laboratory, USA



This book describes a fast, accurate and flexible modelling methodology for PCBs. With its systematic approach to addressing tensorial analysis of networks (TAN) modelling methods, the book provides information and solutions to the designers and manufacturers of analogue, RF, digital and mixed signal electronic circuits and systems.

2020 / 400pp / £125 / \$160

Print PBCS0720 / 978-1-83953-049-4

eBook PBCS072E / 978-1-83953-050-0

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| Advanced Materials and Technologies for Next Generation Integrated Circuits | Srivastava & Mohanty (Eds) | £115 | \$150 | 978-1-78561-664-8 | 978-1-78561-665-5 | PBCS049 | 2020 |
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